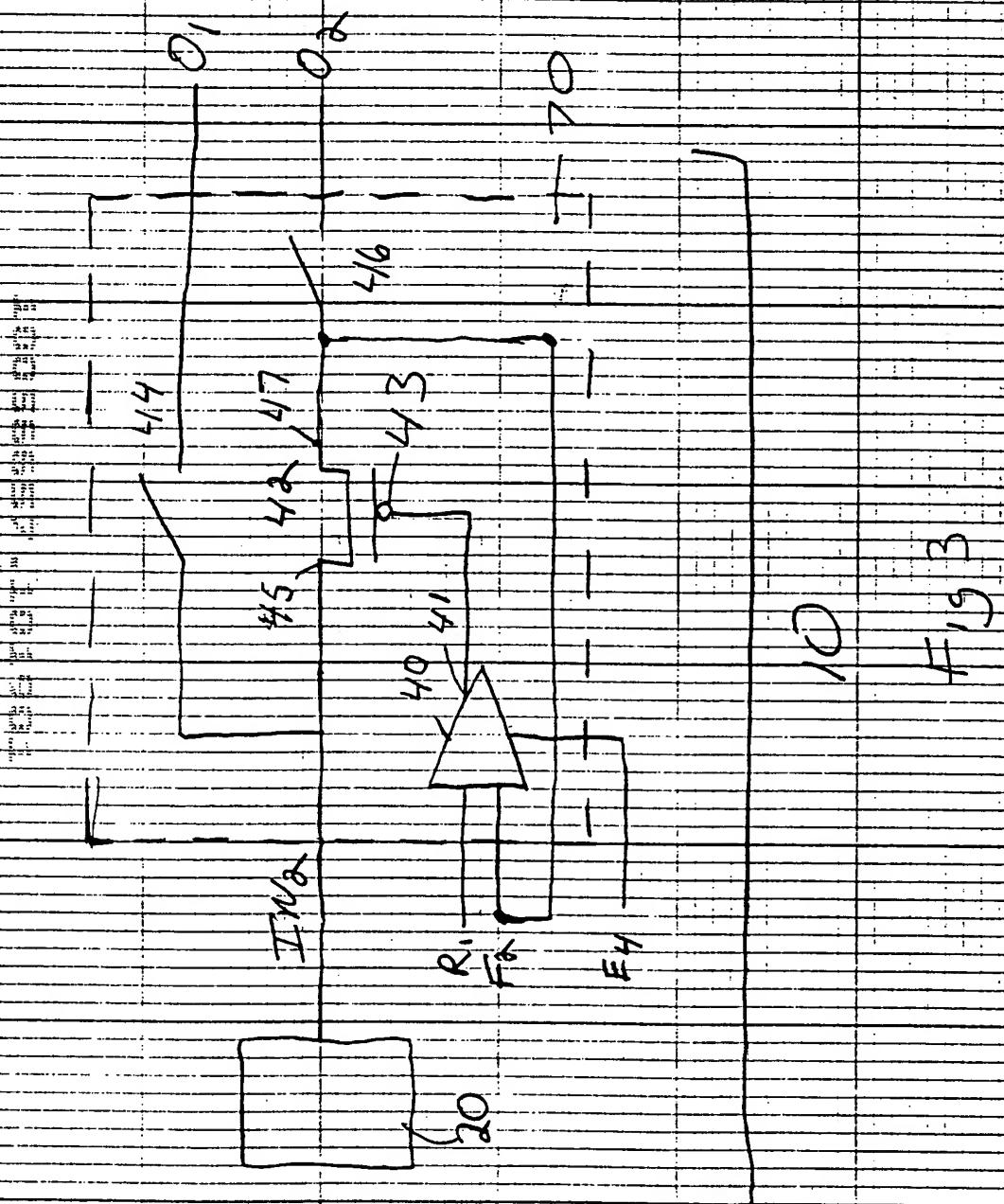


Fig 2



IN_2

R_1

E_4

F_2

EN
40
41

Switch 5

56

43

58

Switch 13

45 42 u1

46

Switch 1

46

Vcc

60

Switch 7

O_1

62

Switch 6

O_2

48

50

52

54

66

7.0

Fig. 4